

IN THE SPECIFICATION

Please amend the specification as follows:

On page 2, first full paragraph, amend as follow:

In a first embodiment, the invention audio boost circuit has an input buffer responsive to a program input signal having high, low and mid-range frequency signal components for providing a buffered program signal. The buffered program signal is fed to an all pass phase inverter having an input coupled to receive the buffered program signal and an output providing an inverted buffered program signal[[]]. The buffered program signal is also fed to a Band-pass filter having a predetermined Q, responsive to the buffered program signal for providing an inverted Band-pass boosted program signal. A summing amplifier adds the inverted buffered program signal to the inverted Band-pass boosted program signal to provide a composite program signal as an output signal to a power amplifier and speaker combination. In a more particular embodiment, the Band-pass filter has a peak gain at a center frequency, and a frequency adjustment means is provided for adjusting the frequency at which the peak gain occurs. In a more particular embodiment, the Band-pass filter has a first second and third resistor and a first and second capacitor. The Band-pass filter's first, second and third resistor values and the values of the first and second capacitors are selected to obtain a Q in the range of from 3 to 6. A frequency adjustment resistor in series with the second resistor is adjusted to position the peak gain of the Band-pass filter at a frequency in the range of 50 to 100 cycles/sec.

Page 11, first full paragraph: please cancel the amendment made to this paragraph in the prior amendment and amend this paragraph as follows (in relation to the originally filed paragraph):

Resistor 64 and 66 in series have a first and second terminal. The first terminal of the series combination is connected to terminal 20 to receive the inverted buffered program signal. The first terminal of the series combination is also connected to the inverting input

of operational amplifier and to the first terminal of the feed-back resistor 62. The second terminal of the series combination is connected terminal 24 to receive the inverted band pass boosted program signal from the band pass filter.